



**austriamicrosystems AG**

**is now**

**ams AG**

The technical content of this austriamicrosystems datasheet is still valid.

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## 5.2 Operating Conditions

Test circuit

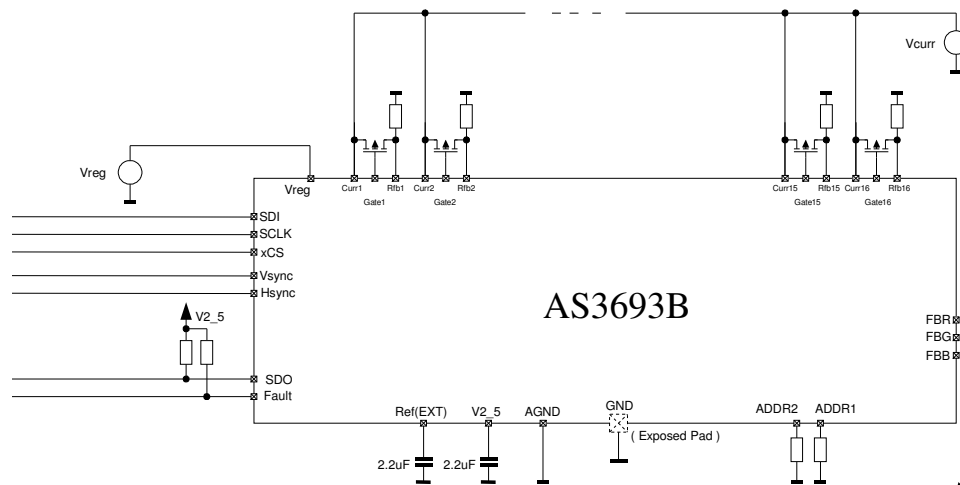


Table 2 – Operating Conditions

Symbol	Parameter	Min	Typ	Max	Unit	Note
VDD	Main Supply			Not Limited	V	Supply is not directly connected to the AS3693B – see section ‘Shunt Regulator’
VDDTOL	Main Supply Voltage Tolerance	-20		+20	%	Applies only for supply VREG is connected via Rvdd
VREGINT	Supply (shunt regulated by AS3693B)	5.0	5.2	5.4	V	If internally (shunt-)regulated by ZD1
VREGEXT	Supply	3	4.5	4.9	V	If externally supplied
VUVL	Undervoltage lockout voltage	2.4	2.5	3	V	If Vreg < UVUL current sources are turned off ( Addr 0x01,Addr 0x02 = 0x00 )
IVREG	Supply Current (Chip current consumption)			20	mA	Excluding current through shunt regulator (ZD1) – see section ‘Shunt Regulator’. Note: Take care of the Power dissipation of the external Resistor.
IVREG_MAX	Maximum supply current			30	mA	Maximum Current Into VREG – PIN (Supply current + shunt regulator current).
IVREG_EXT_OFF	Supply Current (externally supplied, current sources off)			350	uA	Condition: externally supplied Curr_reg1-16 off (register 01h = 00h, register 02h = 00h)
Igate	Gate driving capability	0.5	1	2	mA	Gate1 – Gate16 output current





























































